

Figure 1

Full-Duplex Current Loop
(Prior Art)

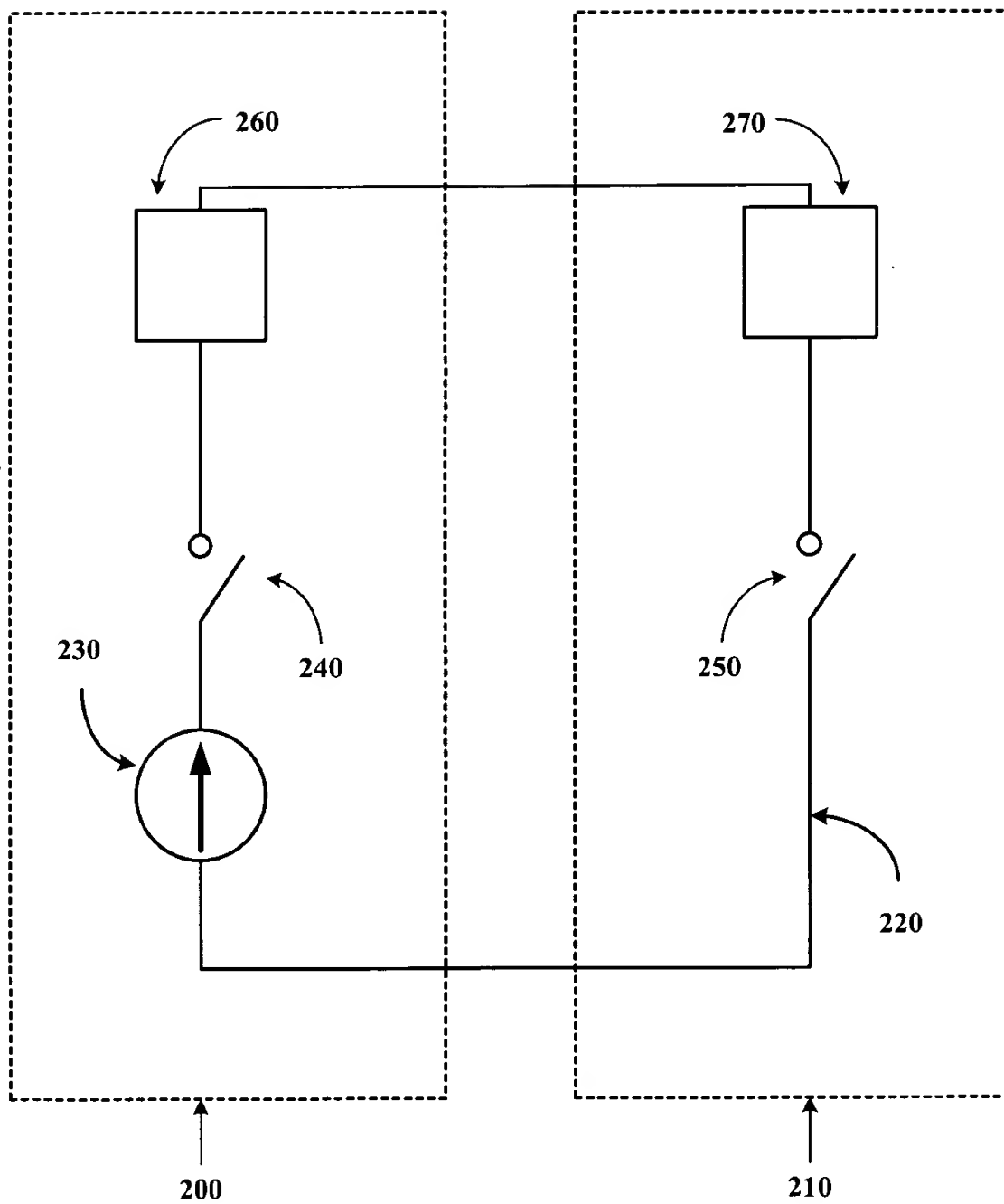
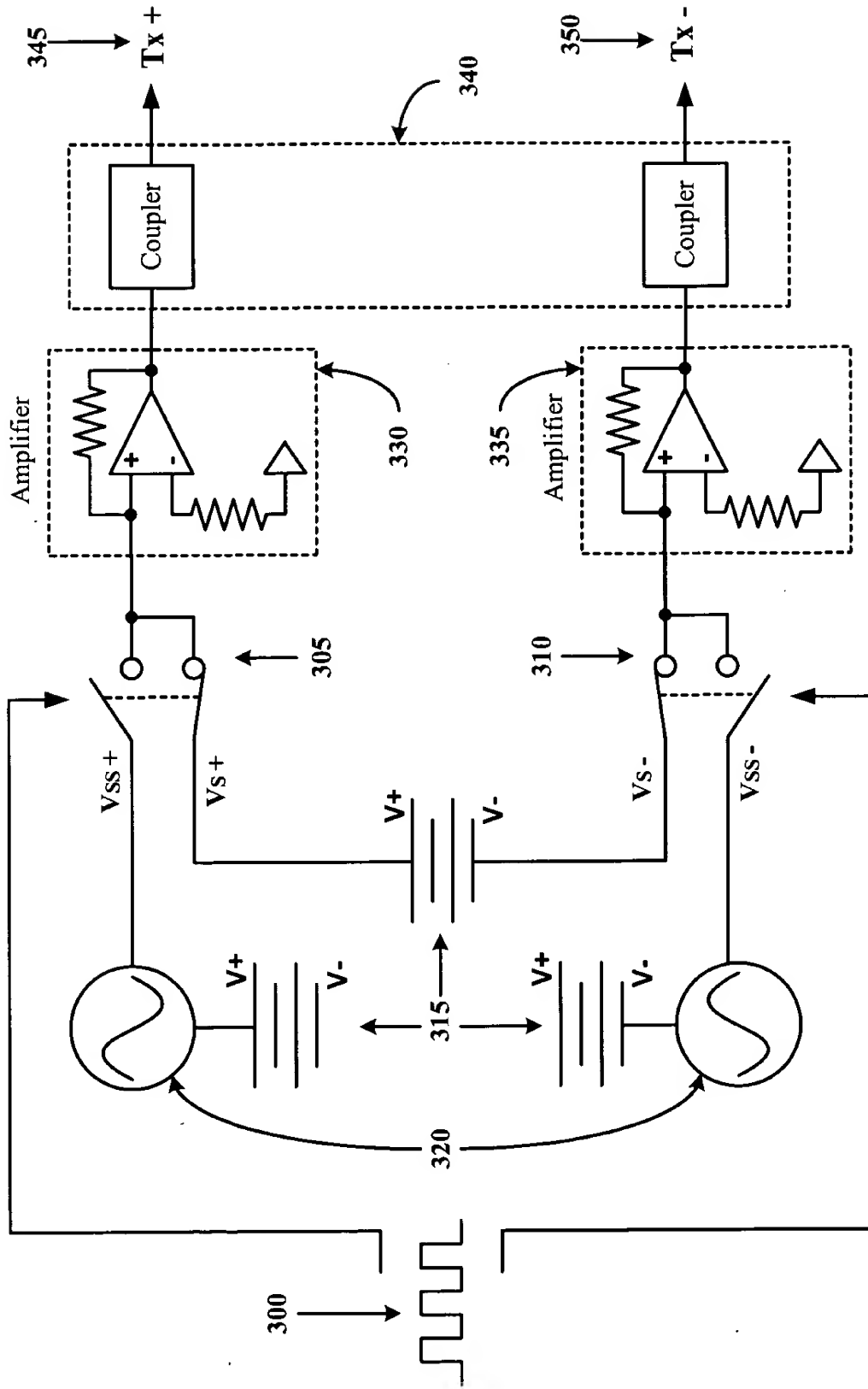


Figure 2

Simplex Current Loop
(Prior Art)



Transmitter

Figure 3

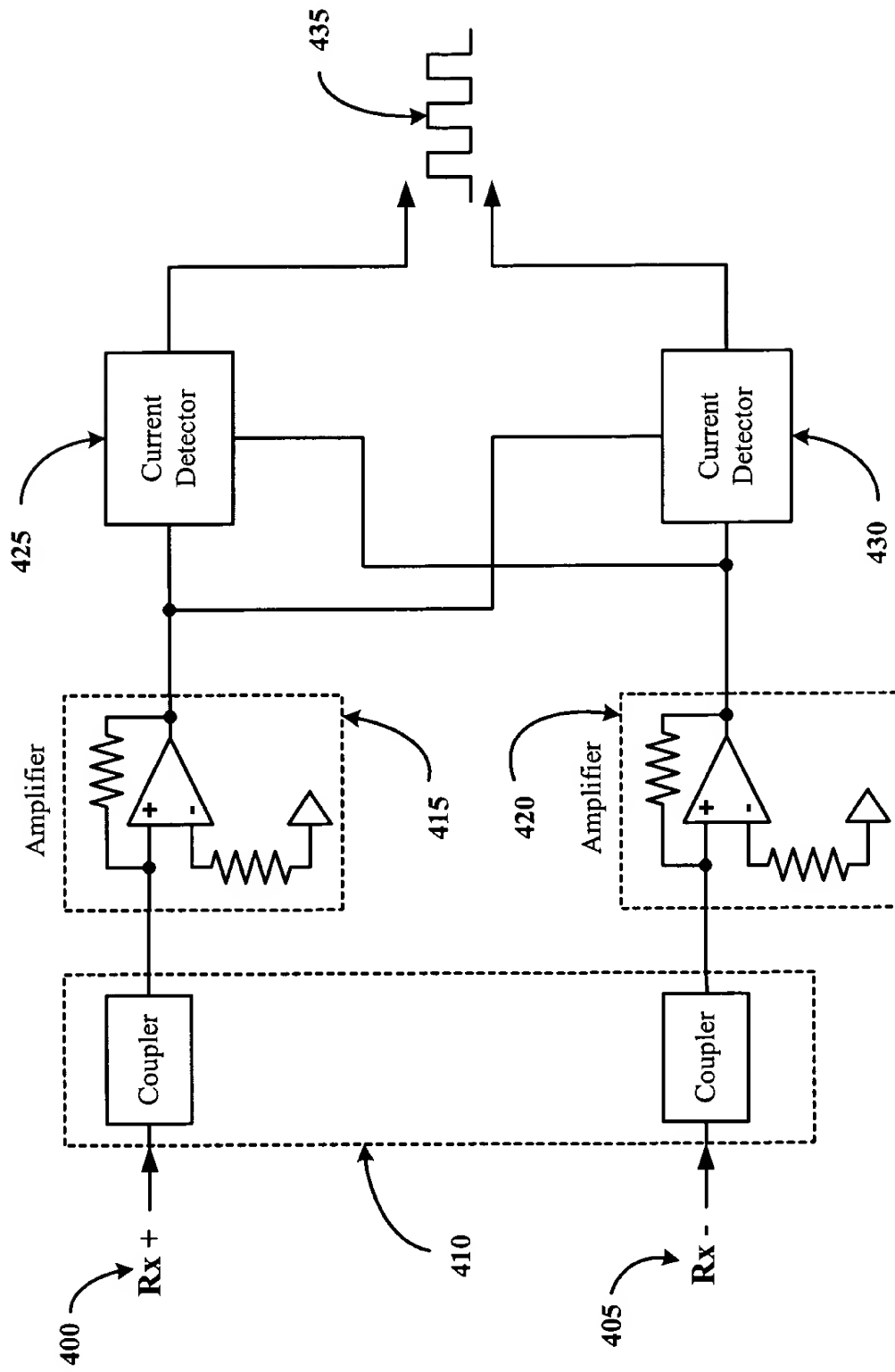
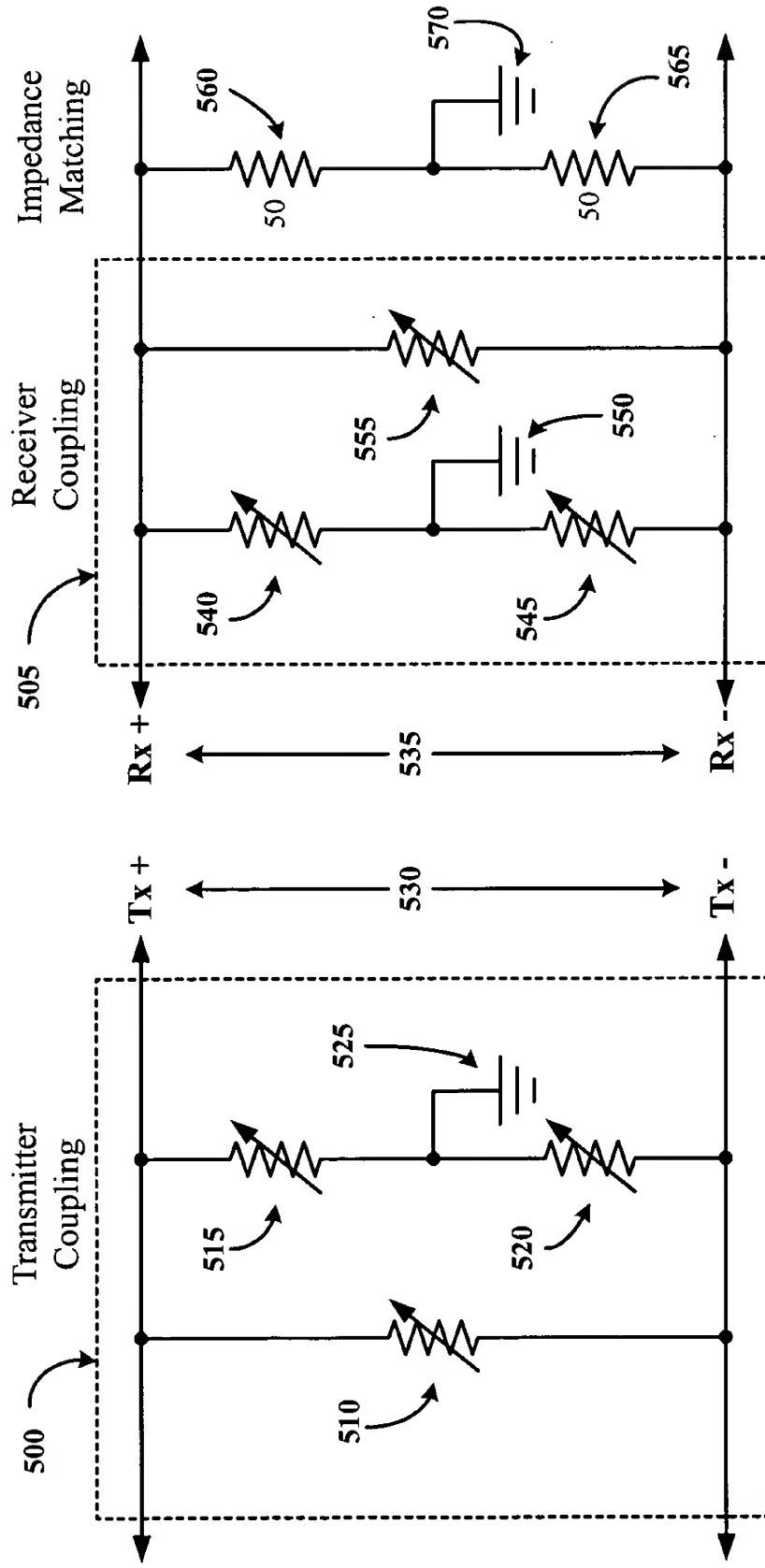


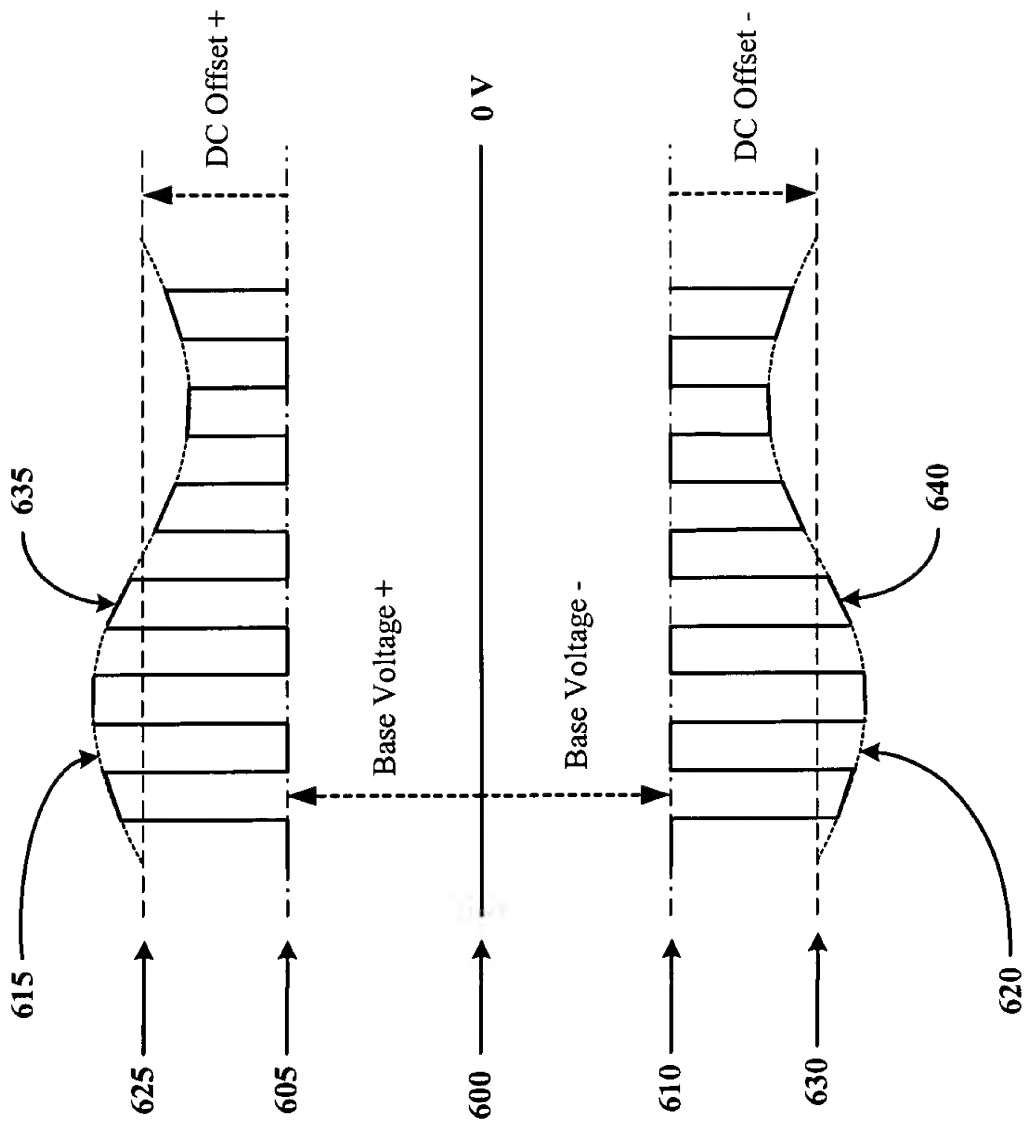
Figure 4

Receiver



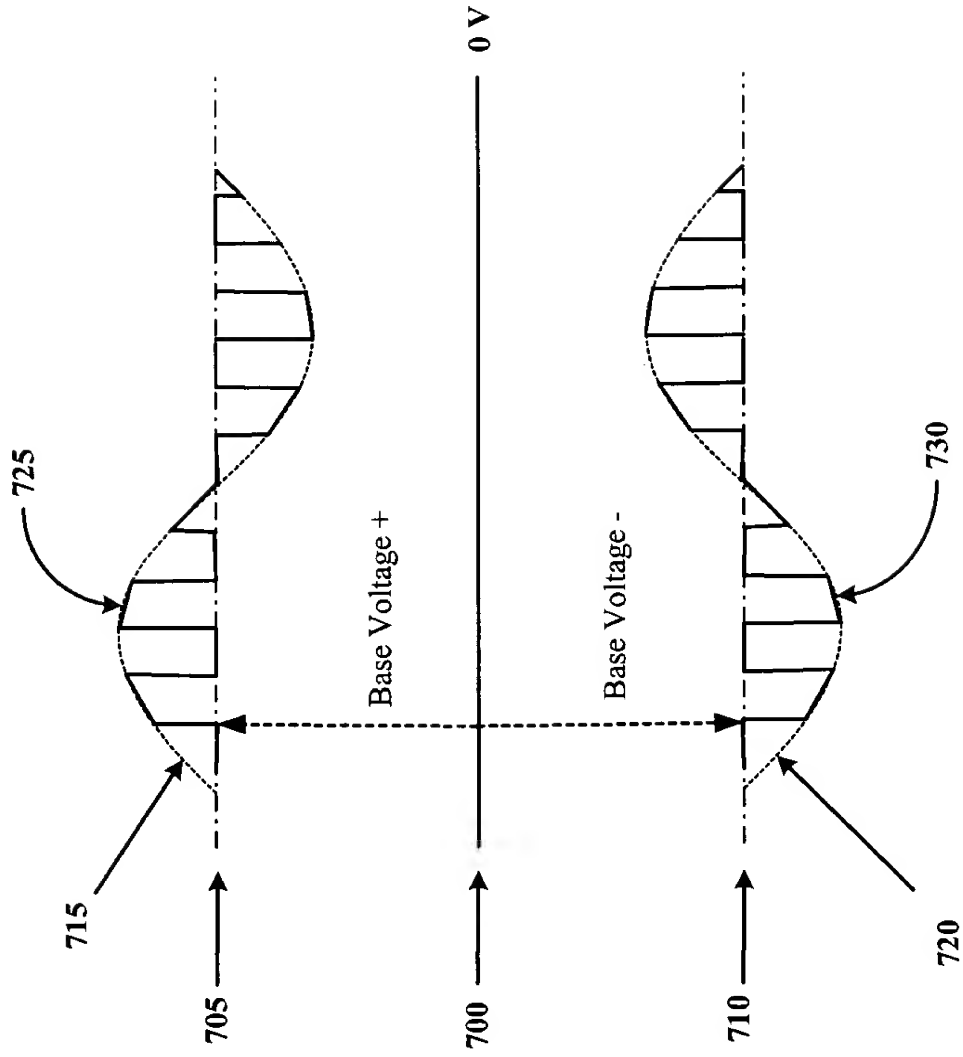
Twisted Pair
Coupling

Figure 5



Transmitted Signal
With Offset Voltage

Figure 6



Transmitted Signal
Without Offset Voltage

Figure 7